

PTO-1449		Application No. 10/629,020		Applicant R. William Ezell	
Information Disclosure Citation In an Application		Docket Number 073671.0186	Group Art Unit 2631	Filing Date July 28, 2003	

DEC 28 2004

U.S. PATENT DOCUMENTS

DOCUMENT & TRADEMARK OFFICE	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
SL A	5,701,099	12/23/97	Shafir	327	552	11/27/95
SL B	6,466,090 B1	10/15/02	Giuroiu	330	86	11/06/00
SL C	2004/0100322 A1	05/27/04	Ganeshan et al.	327	553	11/25/02
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						YES	NO
SL Q	WO 96/27239	09/06/96	PCT	H03G	3/00		
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	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
T	Loh, et al., "A Versatile Digitally Controlled Continuous-Time Filter Structure with Wide-Range and Fine Resolution Capability," IEEE, Vol. 30, No. 5, pp. 265-276	May 1992
U	PCT search report for PCT/US2004/021579, 15 pages	Dec. 14, 2004
V		

EXAMINER /Siu Lee/	DATE CONSIDERED 12/13/2006
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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DAL01:831801.1

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	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
SL A	US 6,304,135 B1	10/16/2001	John Matthew Muza	327	553	11/17/1999
SL B	5,914,633	06/22/1999	Vittorio Comino et al.	327	553	08/08/1997
SL C	6,112,125	08/29/2000	Randall L. Sandusky	700	28	03/08/1995
SL D	5,352,934	10/04/1994	Sakhawat Khan	307	296.1	03/25/1993
SL E	US 2003/0090316 A1	05/15/2003	Lutz Dathe et al.	327	554	06/27/2002
SL F	5,245,646	09/14/1993	H. Spence Jackson et al.	377	2	06/01/1992
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SL O	EP 1 172 933 A2	01/16/2002	Europe	H03H	11/04	YES XX
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	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
S	PCT/US2004/021750, Notification of Transmittal of the International Search Report and the Written Opinion of the International Searching Authority, or the Declaration, 8 pgs					01/26/2005
T						
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DAL01:839209.1

PTO-1449	Application No. 10/629,020	Applicant(s) R. William Ezell
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	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
SL O	Geiger, et al., "VLSI Design Techniques for Analog and Digital Circuits," McGraw-Hill Publishing Company, pp. 651-656 plus title and copyright pages	1990
SL P	Martinez, et al., "A 60-mW 200-MHz Continuous-Time Seventh-Order Linear Phase Filter With On-Chip Automatic Tuning System," IEEE Journal of Solid-State Circuits, Vol. 38, No. 2, pp. 216-225 plus cover page	02/2003
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		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
SL	P	Laker, et al., "Design of Analog Integrated Circuits and Systems," pp. 786-791 plus copyright page	1994
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